

FIG. 1
(PRIOR ART)

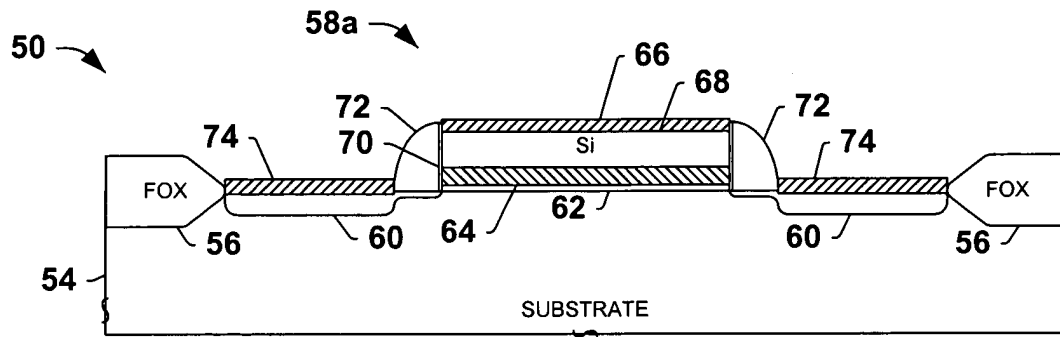


FIG. 2A

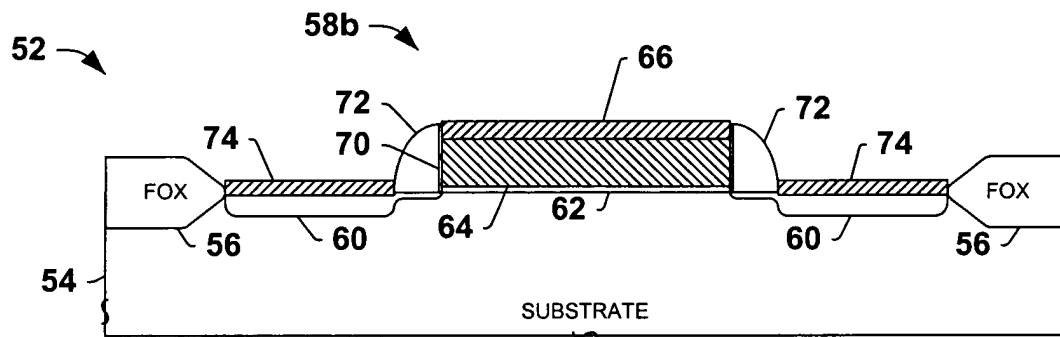


FIG. 2B

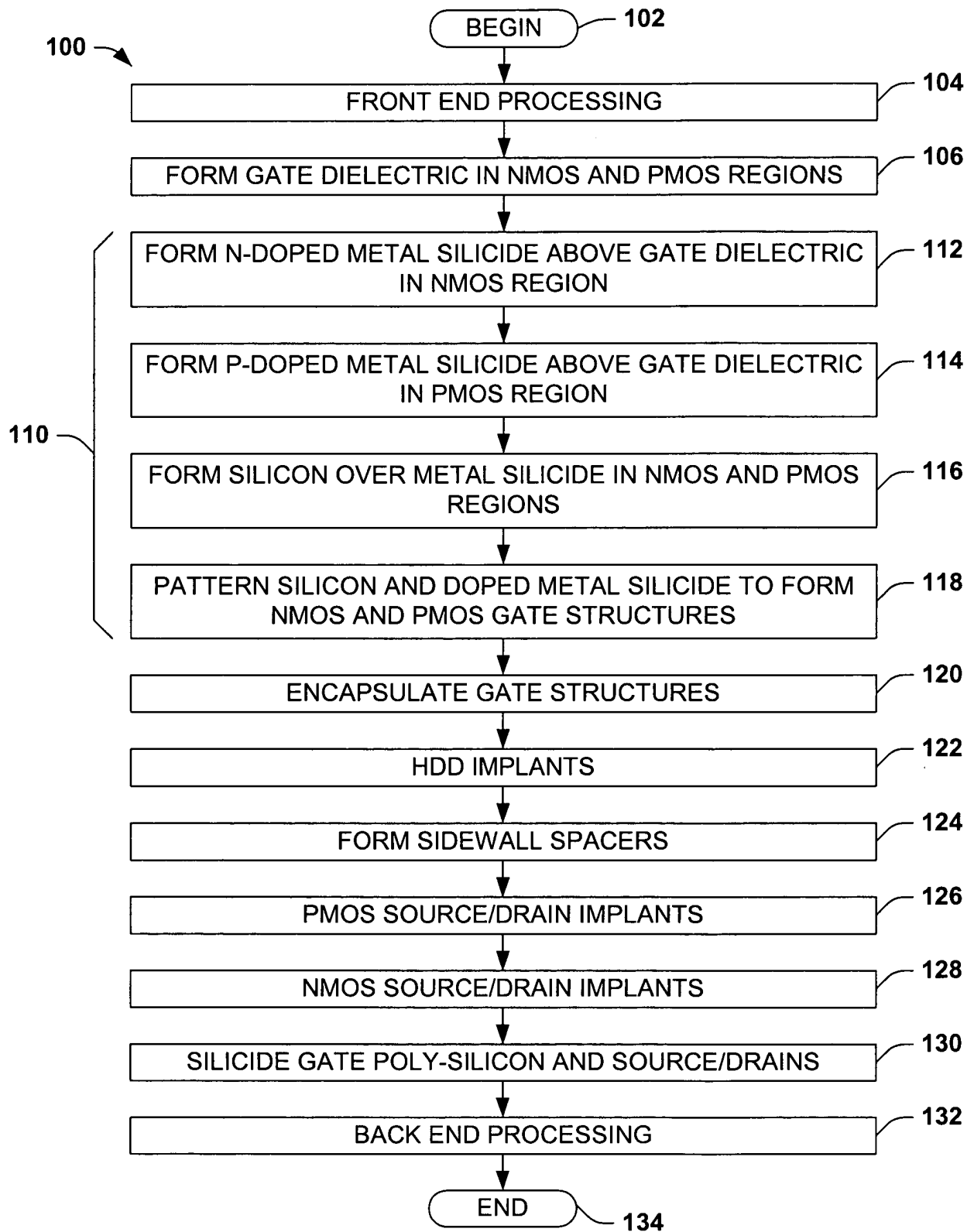


FIG. 3

110a

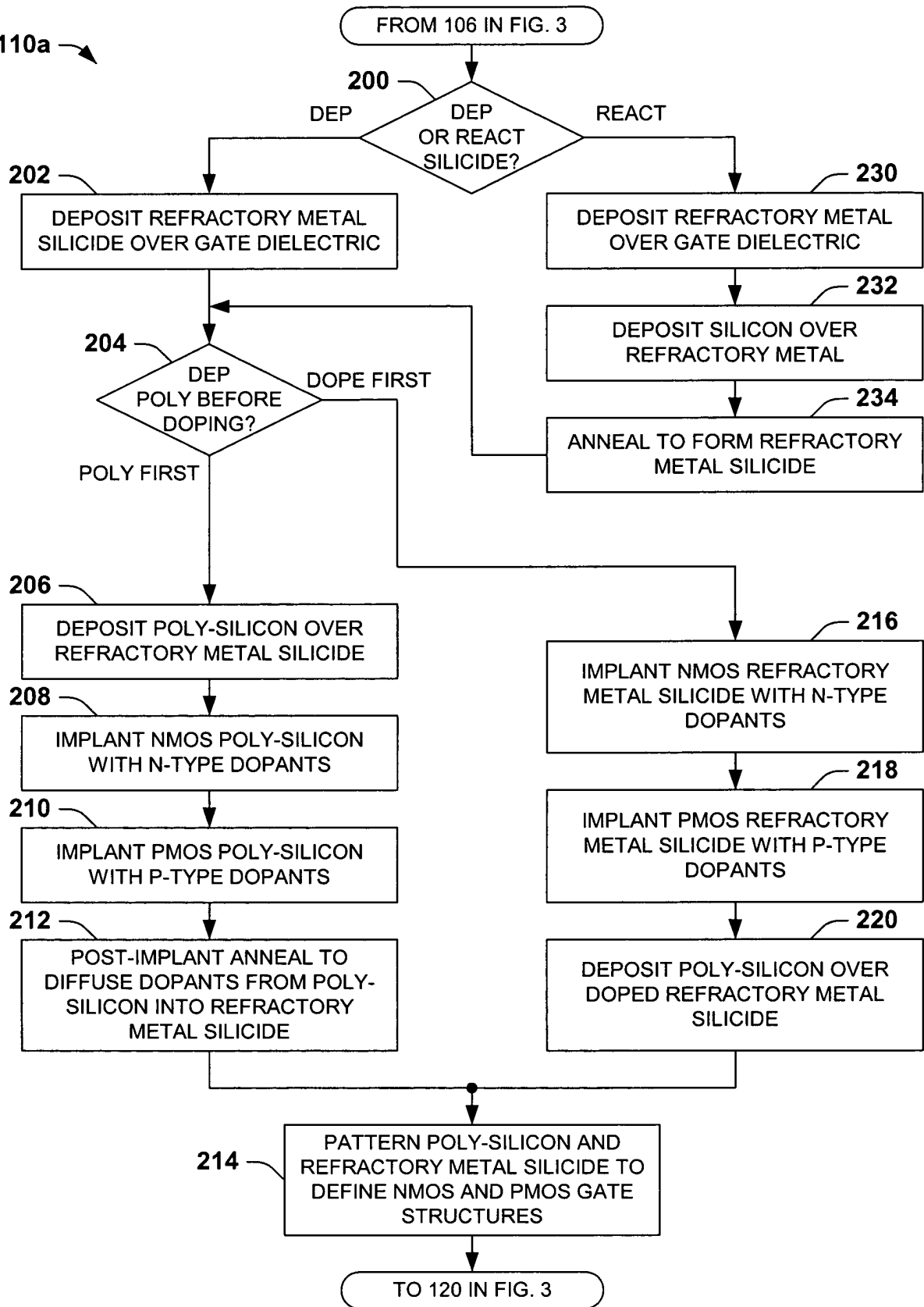


FIG. 4

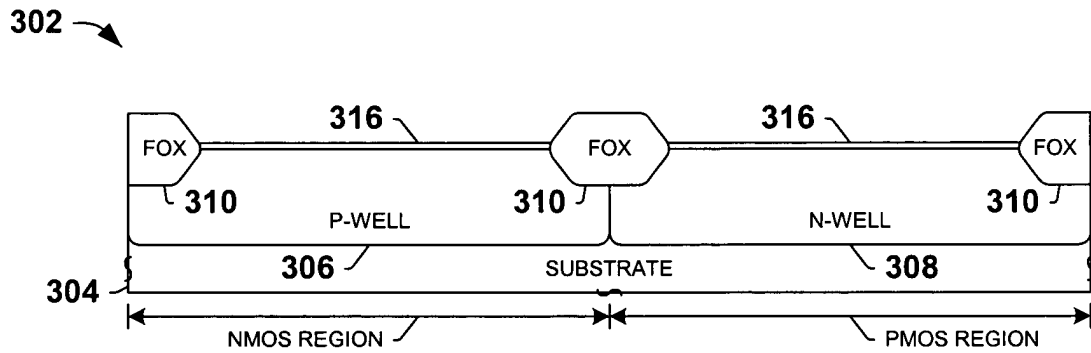


FIG. 5A

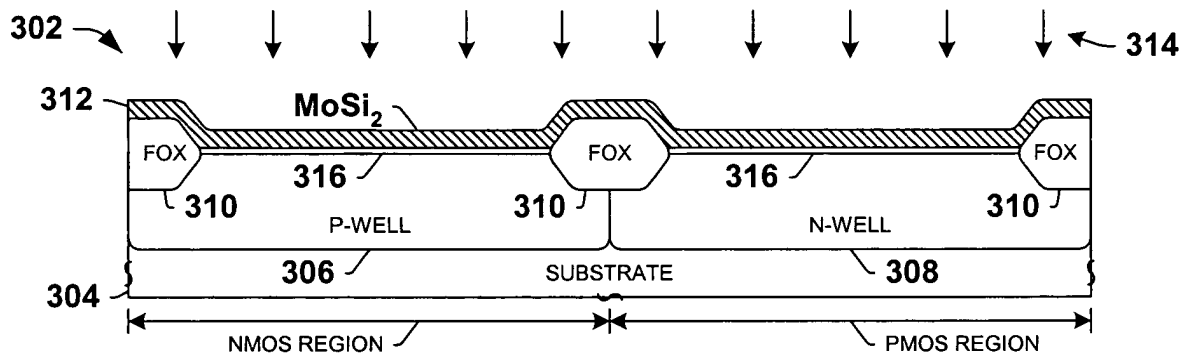


FIG. 5B

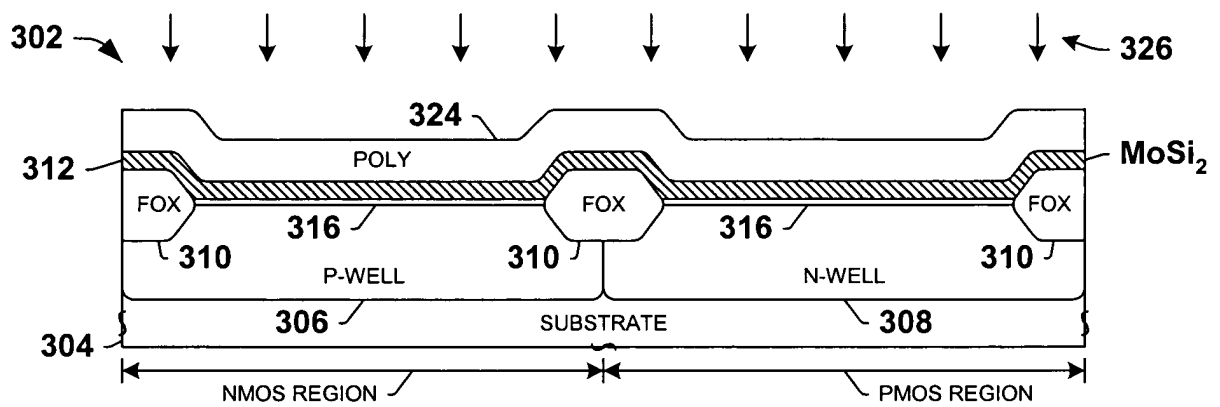


FIG. 5C

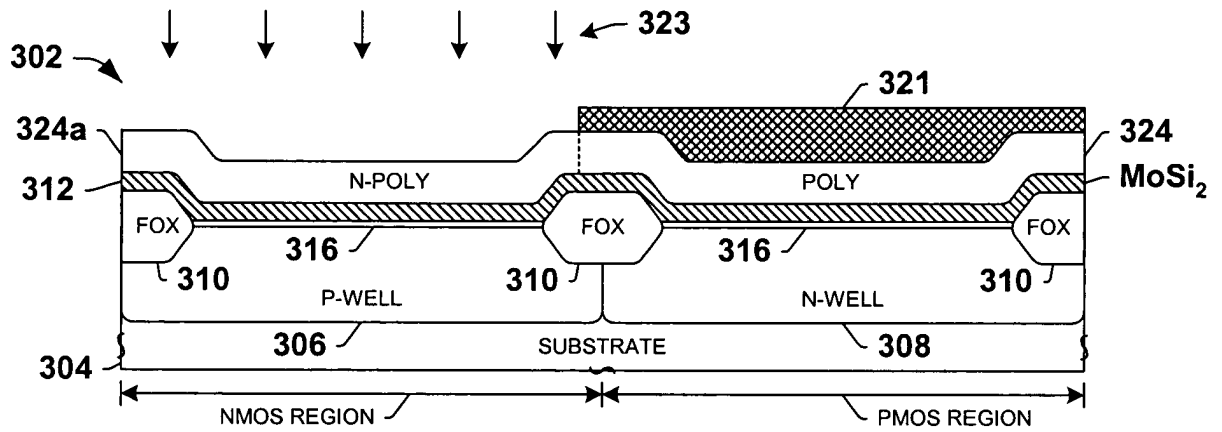


FIG. 5D

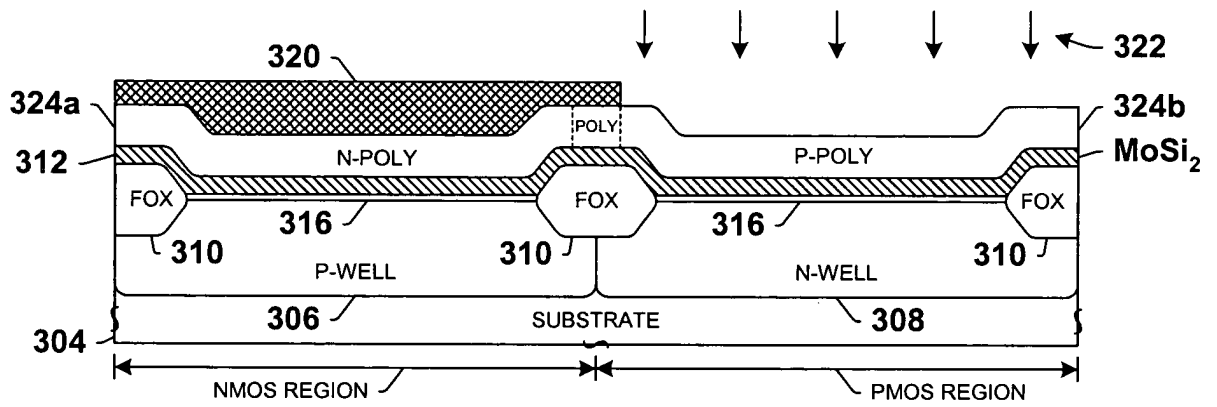


FIG. 5E

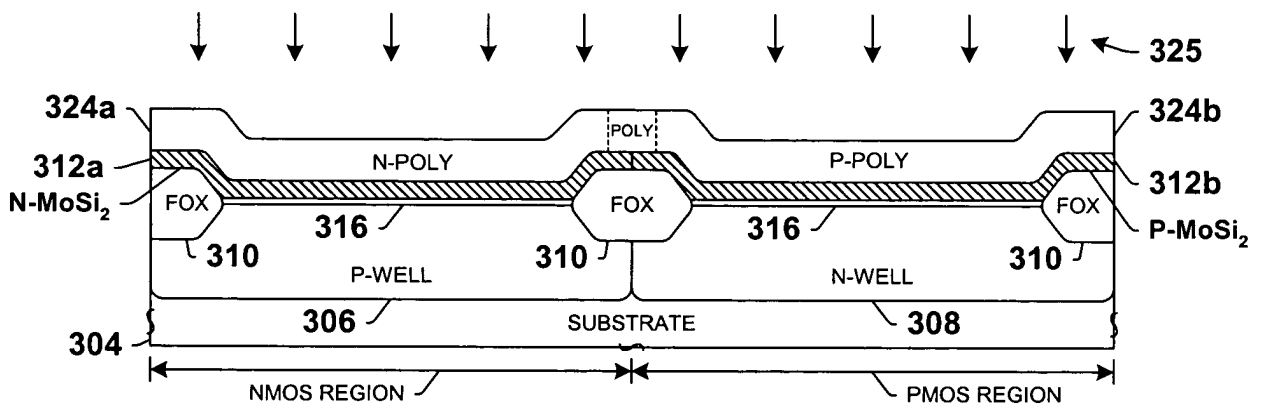


FIG. 5F

This diagram shows a cross-sectional view of a semiconductor device. The substrate 304 is divided into an NMOS REGION 306 and a PMOS REGION 308. The NMOS REGION 306 includes a P-WELL 338 and an N-POLY gate 332. The PMOS REGION 308 includes an N-WELL 336 and a P-POLY gate 332. Both regions have FOX (field oxide) regions 310 and 312. The device is covered by a layer 302, with arrows 334 indicating light or radiation incident on the surface.

302 340 332 320 324a 340 312a FOX 310 338 P-WELL 306 SUBSTRATE 304 NMOS REGION

342a 324b 340 312b FOX 310 336 N-WELL 308 PMOS REGION

FIG. 5I

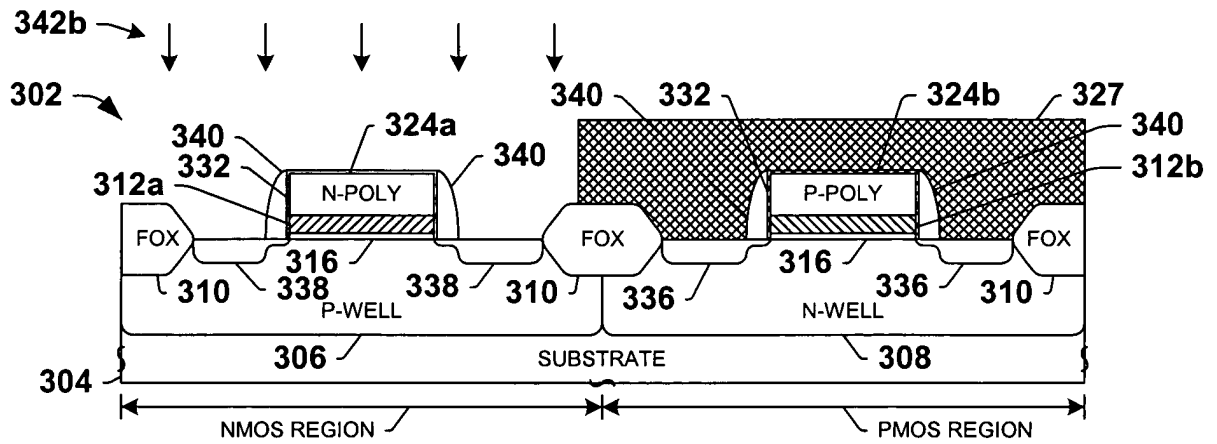


FIG. 5J

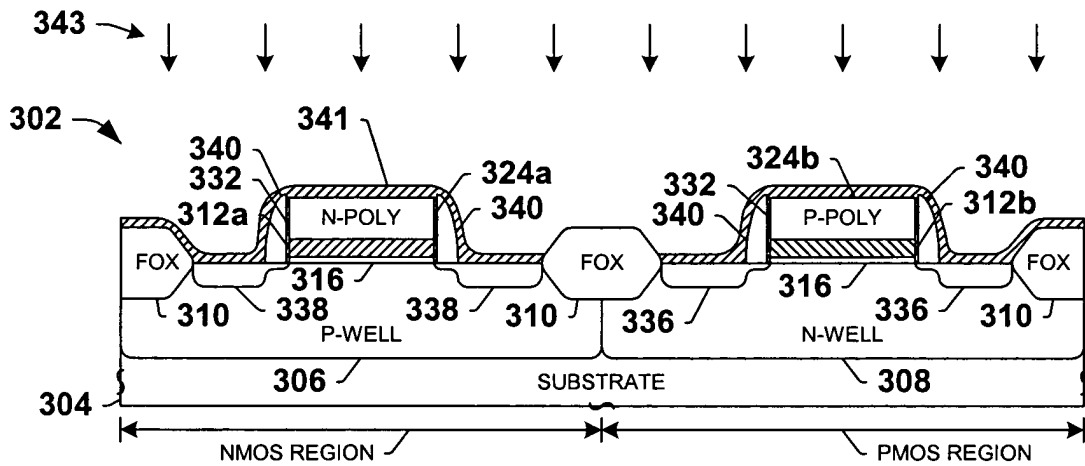


FIG. 5K

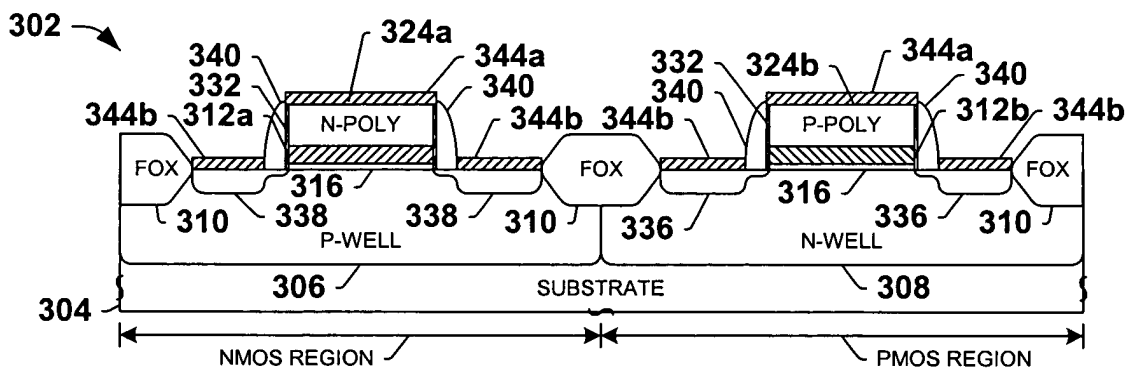


FIG. 5L

A cross-sectional view of a semiconductor device 302a. The device is built on a substrate 304, which is divided into an NMOS REGION and a PMOS REGION. The NMOS REGION contains an N-MOSFET 306, and the PMOS REGION contains a P-MOSFET 308. Both transistors have a gate stack 340 on top, consisting of a gate oxide 341 and a gate electrode 342. The NMOSFET 306 has a channel region 310, source/drain regions 312a and 312b, and a P-WELL 316. The PMOSFET 308 has a channel region 310, source/drain regions 312a and 312b, and an N-WELL 316. The device is surrounded by a FOX (field oxide) layer 338. The substrate 304 is labeled with 306 and 308. The NMOS REGION and PMOS REGION are indicated by brackets at the bottom.

[illegible]

FIG. 50

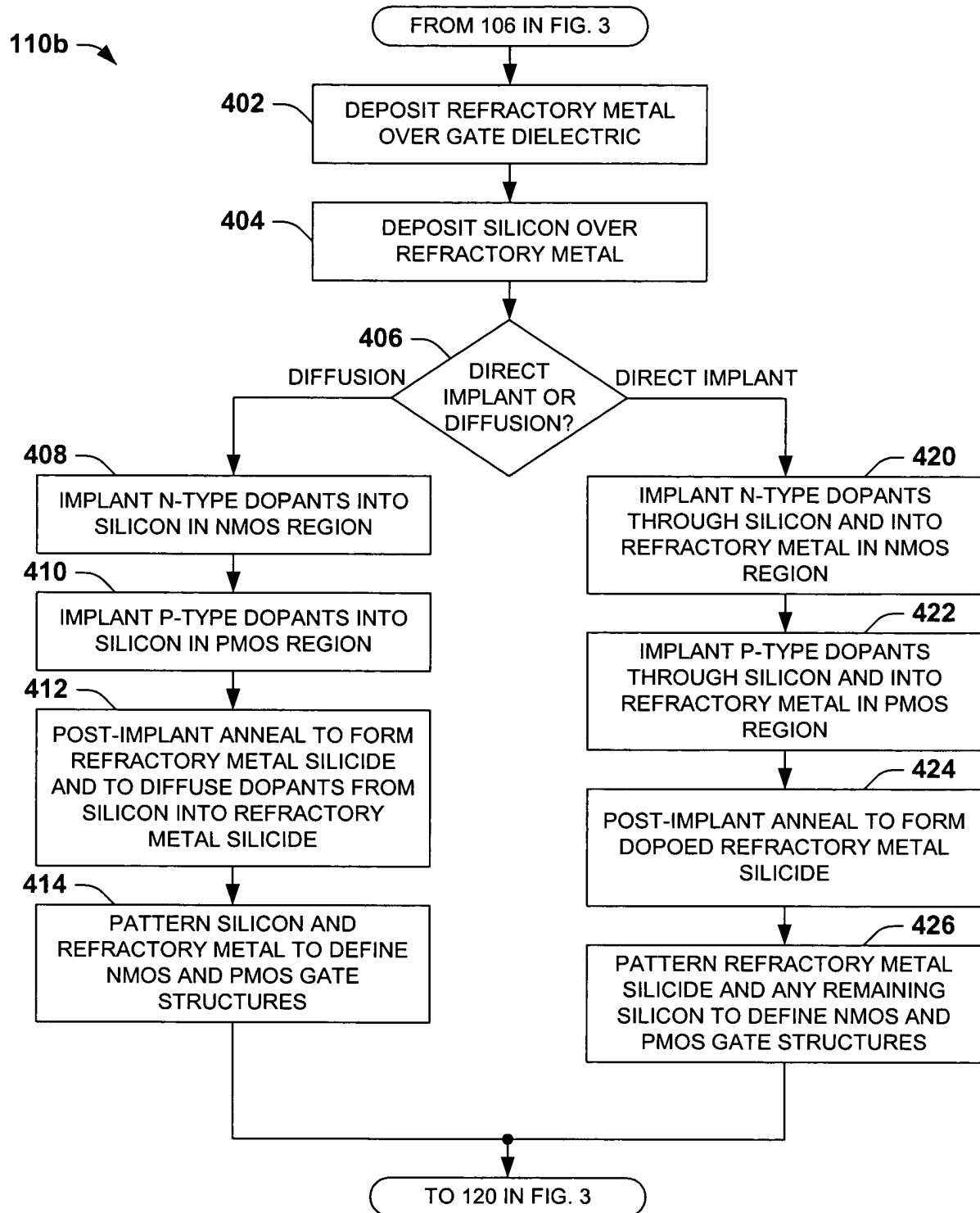


FIG. 6

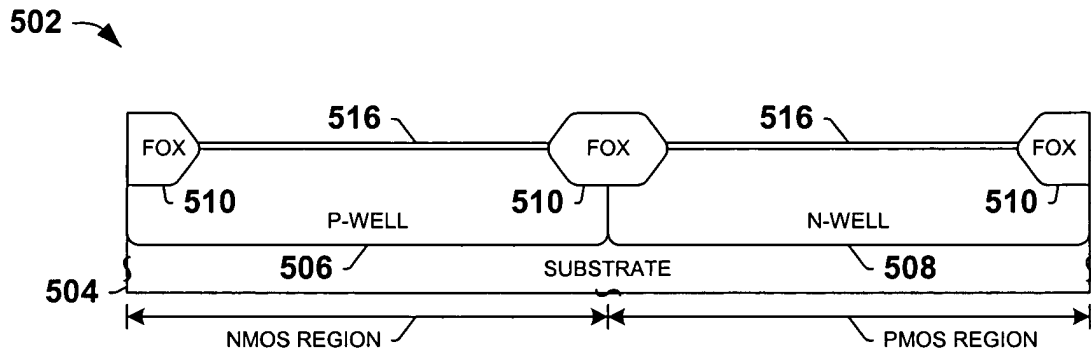


FIG. 7A

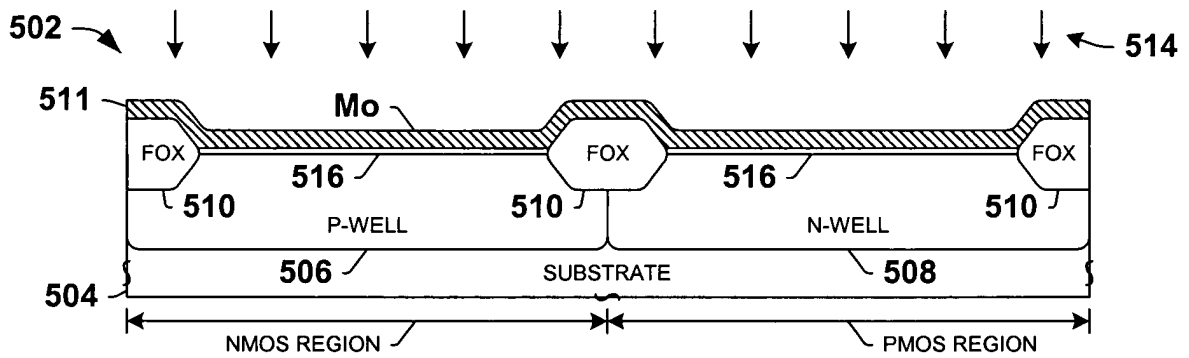


FIG. 7B

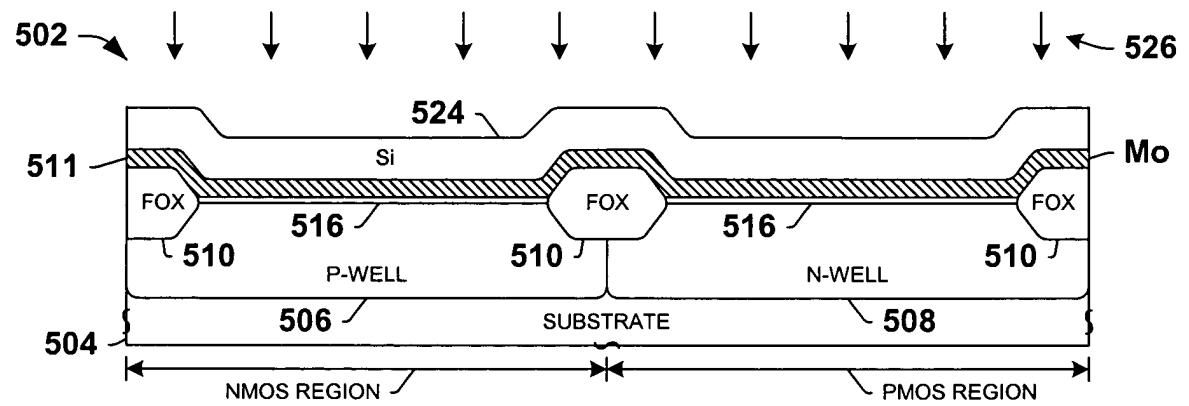


FIG. 7C

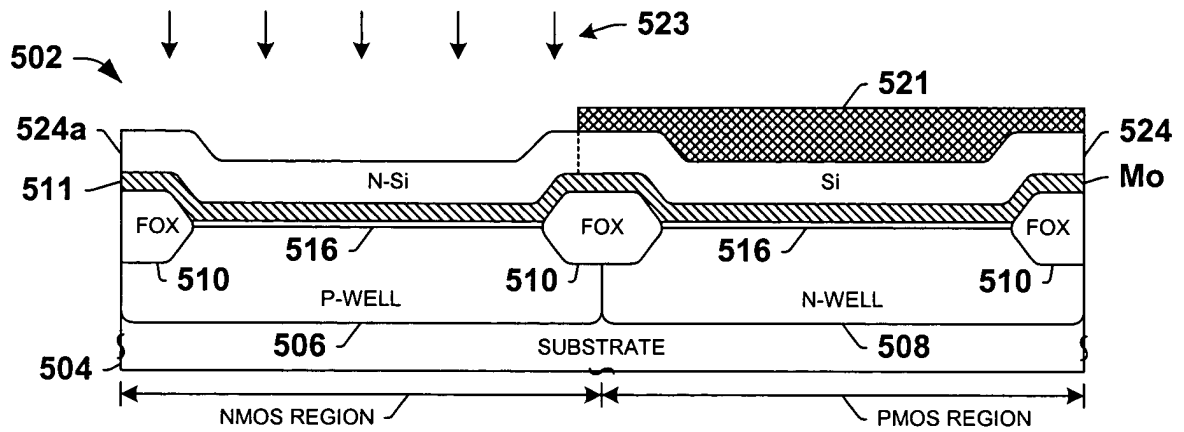


FIG. 7D

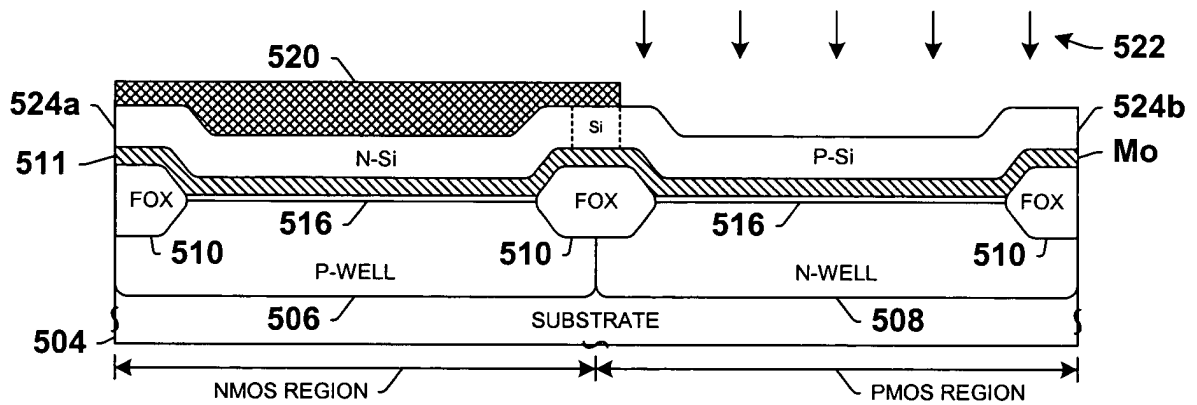


FIG. 7E

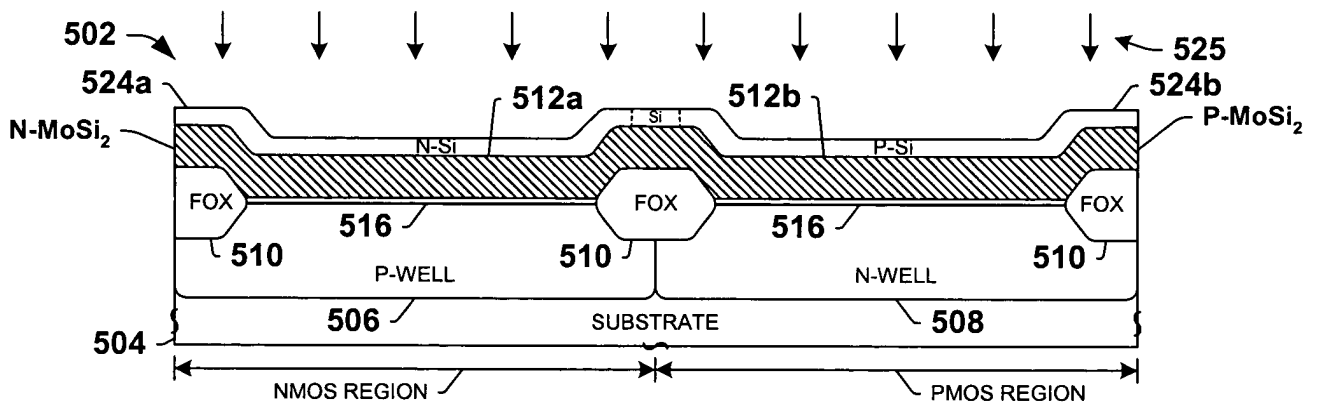


FIG. 7F

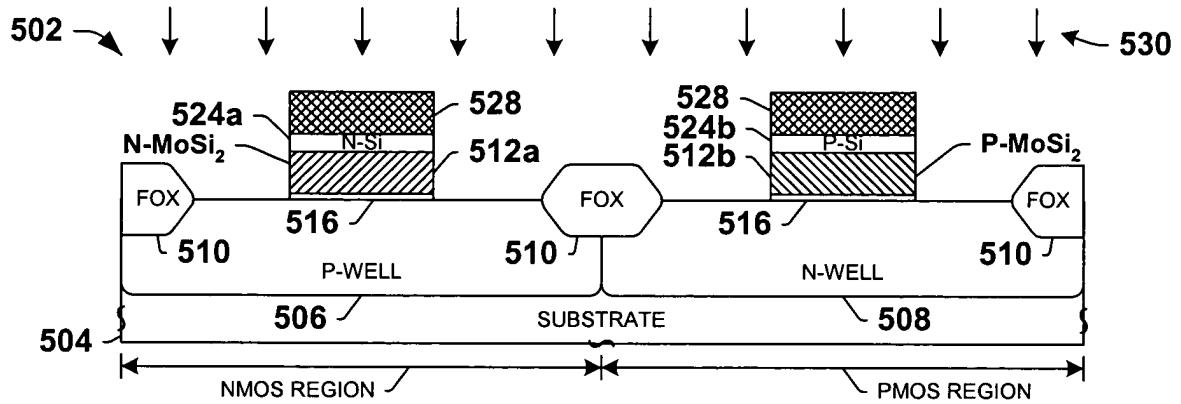


FIG. 7G

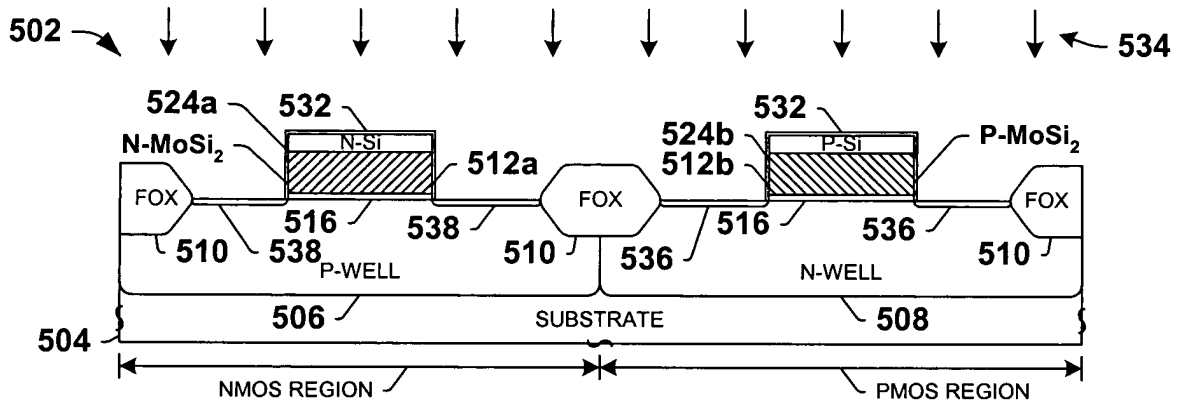


FIG. 7H

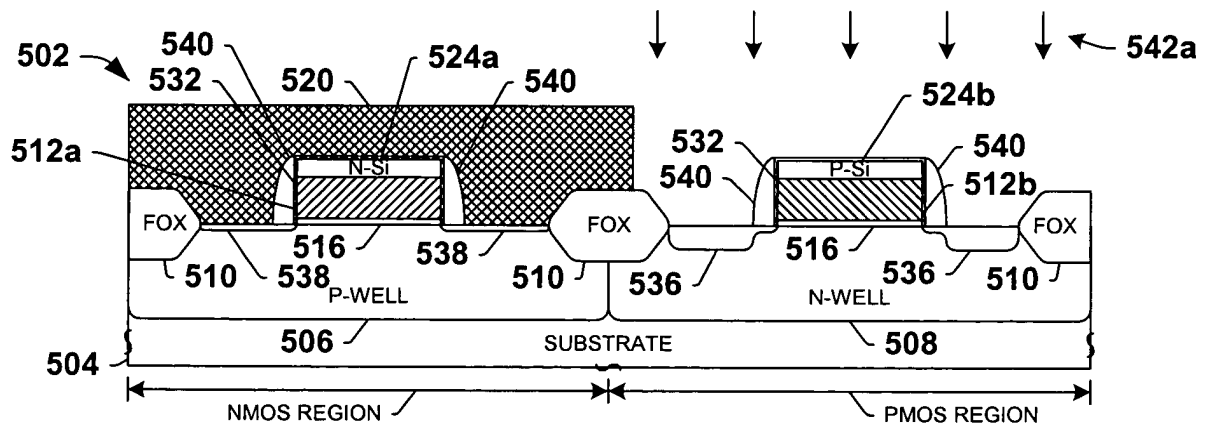


FIG. 7I

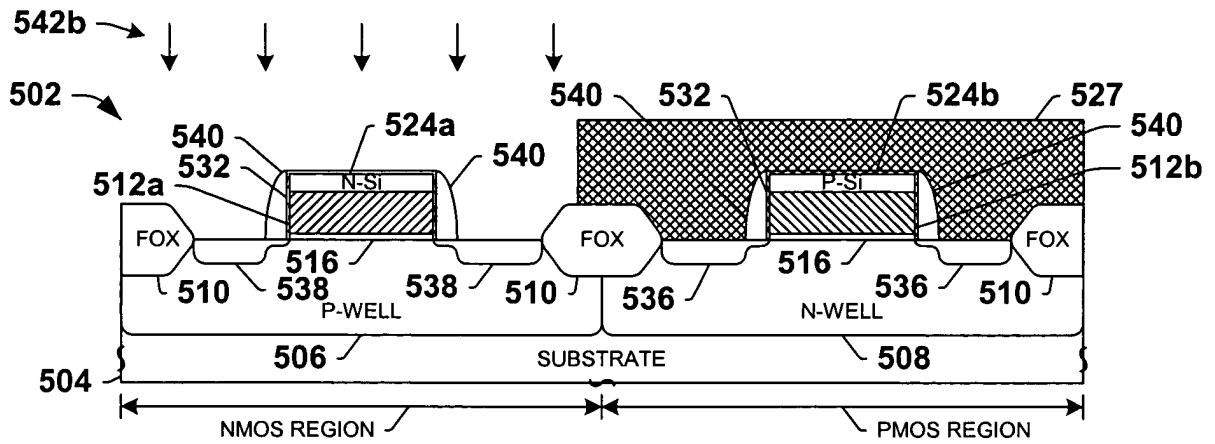


FIG. 7J

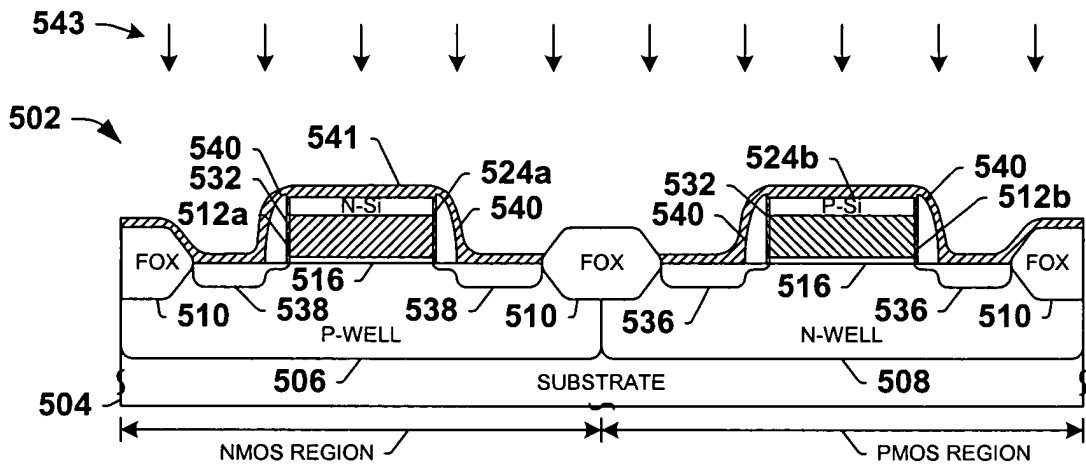


FIG. 7K

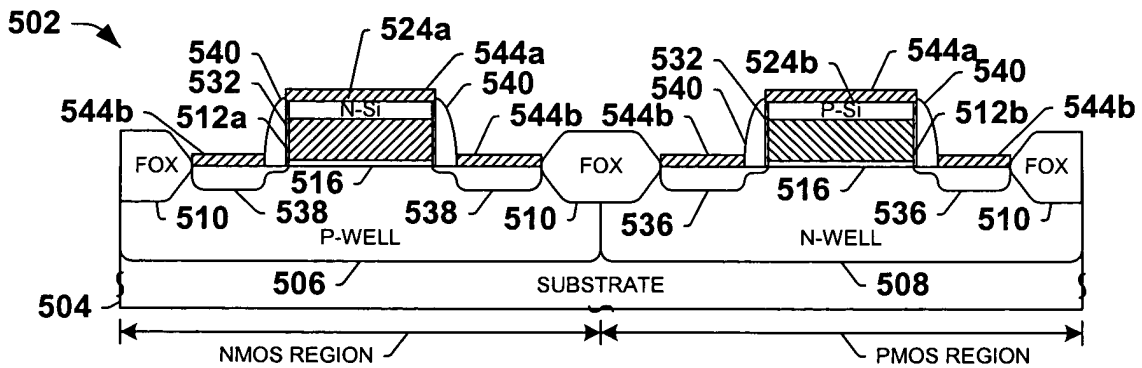


FIG. 7L

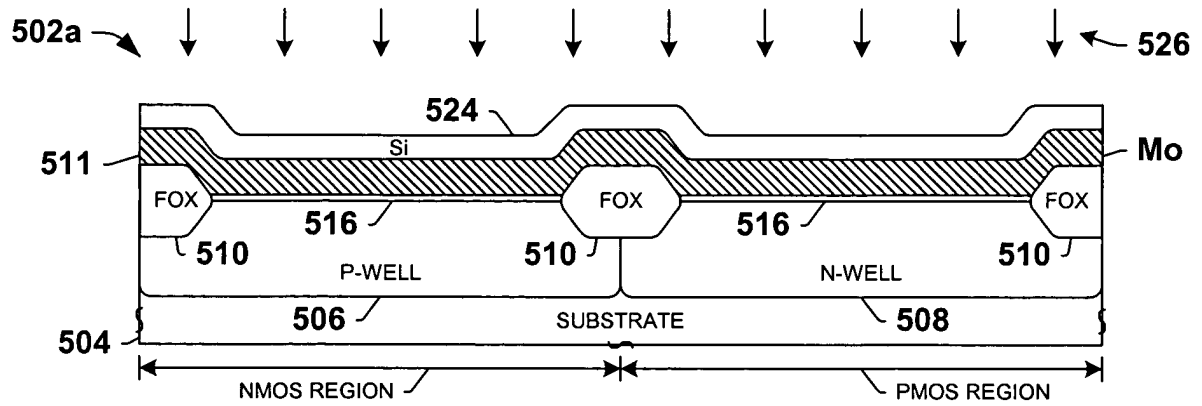


FIG. 7M

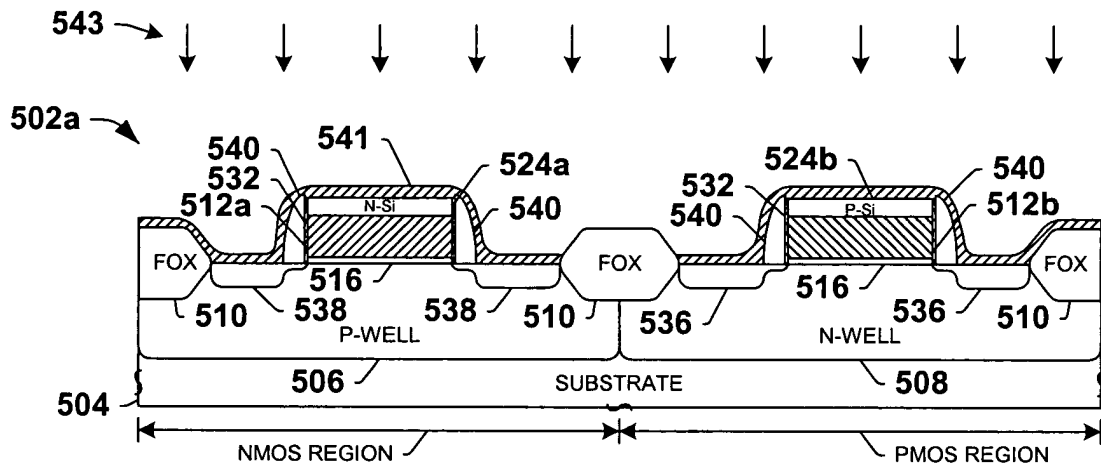


FIG. 7N

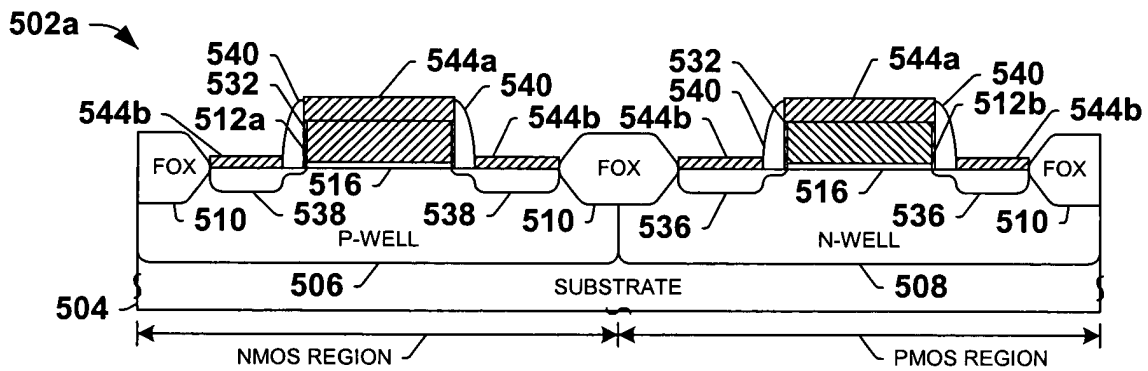


FIG. 7O